A3515 and A3516

Ratiometric, Linear Hall-Effect Sensors for High-Temperature Operation

Not for New Design

These parts are in production but have been determined to be NOT FOR NEW DESIGN. This classification indicates that sale of this device is currently restricted to existing customer applications. The device should not be purchased for new design applications because obsolescence in the near future is probable. Samples are no longer available.

Date of status change: May 2, 2005

Recommended Substitutions:

For new customers or new applications: • for the A3515, please refer to the Allegro <u>A1321</u>, and • for the A3516, please refer to the Allegro <u>A1323</u>.

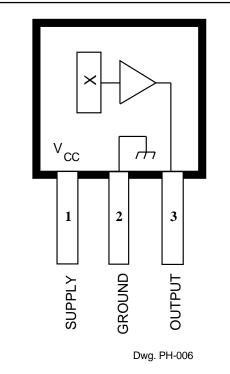
NOTE: For detailed information on purchasing options, contact your local Allegro field applications engineer or sales representative.

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3515 AND 3516

RATIOMETRIC, LINEAR HALL-EFFECT SENSORS FOR HIGH-TEMPERATURE OPERATION



Pinning is shown viewed from branded side.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, V_{CC} 8.0 V
Output Voltage, V_0 8.0 V
Output Sink Current, I ₀ 10 mA
Magnetic Flux Density, B Unlimited
Package Power Dissipation,
P _D See Graph
Operating Temperature Range*, T_A
Suffix 'E-'40°C to +85°C
Suffix 'L-'40°C to +150°C
Storage Temperature Range,
T _S 65°C to +170°C
* Infrequent excursions permitted; see Applications Information.

The A3515– and A3516– are sensitive, temperature-stable linear Halleffect sensors with greatly improved offset characteristics. Ratiometric, linear Hall-effect sensors provide a voltage output that is proportional to the applied magnetic field and have a quiescent output voltage that is approximately 50% of the supply voltage. These magnetic sensors are ideal for use in linear and rotary position sensing systems in the harsh environments of automotive and industrial applications over extended temperatures to -40°C and +150°C. The A3515– features an output sensitivity of 5 mV/G, while the A3516– has an output sensitivity of 2.5 mV/G. See the Magnetic Characteristics table for complete, individual device parametrics.

Each BiCMOS monolithic circuit integrates a Hall element, improved temperature-compensating circuitry to reduce the intrinsic sensitivity drift of the Hall element, a small-signal high-gain amplifier, and a rail-to-rail lowimpedance output stage.

A proprietary dynamic offset cancelation technique, with an internal high-frequency clock, reduces the residual offset voltage, which is normally caused by device overmolding, temperature dependancies, and thermal stress. This technique produces devices that have an extremely stable quiescent output voltage, are immune to mechanical stress, and have precise recoverability after temperature cycling. Many problems normally associated with low-level analog signals are minimized by having the Hall element and amplifier in a single chip. Output precision is obtained by internal gain and offset trim adjustments during the manufacturing process.

These devices are supplied in a 3-pin ultra-mini-SIP 'UA' package.

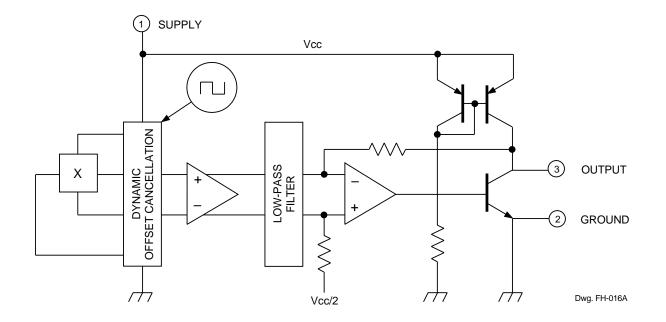
FEATURES

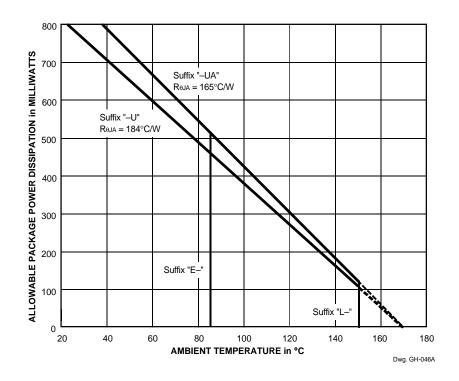
- Temperature-Stable Quiescent Output Voltage
- Precise Recoverability After Temperature Cycling
- Output Voltage Proportional to Applied Magnetic Field
- Ratiometric Rail-to-Rail Output
- Improved Sensitivity
- 4.5 V to 5.5 V Operation
- Immune to Mechanical Stress
- Small Package Size
- Solid-State Reliability

Always order by complete part number, e.g., A3515LUA .



FUNCTIONAL BLOCK DIAGRAM







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ELECTRICAL CHARACTERISTICS over operating temperature range, at V_{cc} = 5 V (unless otherwise noted).

				Limits		
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{cc}	Operating	4.5	5.0	5.5	V
Supply Current	I _{cc}	B = 0, V _{CC} = 6 V, I _O = 0	-	7.2	10	mA
Quiescent Voltage Output	V _{oq}	B = 0, I _O = 1 mA, T _A = 25°C	2.425	2.500	2.575	V
Output Voltage	V _{OH}	B = +X*, I _o = 1 mA	_	4.7	_	V
	V _{OL}	B = -X*, I _o = -1 mA	-	0.2	_	V
Output Source Current Limit	I _{olm}	B = -X*, V _O = 0	-1.0	-1.5	_	mA
Bandwidth (-3 dB)	BW		_	30	_	kHz
Clock Frequency	f _c		-	170	_	kHz
Output Resistance	r _o	I ₀ ≤ -2 mA	-	1.0	_	Ω
Wide-Band Output Noise (rms)	e _o	B = 0, BW = 10 Hz to 10 kHz, I _O ≤ -1 mA, C _O = 100 pF	-	400	_	μV

NOTE 1 – Typical data is at $T_A = 25^{\circ}C$ and is for design information only.

NOTE 2 - Negative current is defined as coming out of (sourcing) the output.

* This test requires positive and negative fields sufficient to swing the output driver between fully OFF and saturated (ON), respectively. It is NOT intended to indicate a range of linear operation.

MAGNETIC CHARACTERISTICS over operating temperature range, at V_{cc} = 5 V, I_o = -1 mA (unless otherwise noted).

	Part Numbers												
	A3515EUA		A3515LUA		A3516EUA		A3516LUA			1			
Characteristic*	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Operating Temp. Range, T _A	-40	_	+85	-40	_	+150	-40	_	+85	-40	_	+150	°C
Sensitivity at T _A = 25°C	4.50	5.00	5.50	4.50	5.00	5.50	2.25	2.50	2.75	2.25	2.50	2.75	mV/G
$\Delta Sens_{(\Delta T)}$ at T _A = Max.	-2.5	2.5	7.5	-2.5	2.5	7.5	-2.5	2.5	7.5	-2.5	2.5	7.5	%
$\Delta Sens_{(\Delta T)}$ at T _A = Min.	-9.0	-1.3	1.0	-9.0	-1.3	1.0	-9.0	-1.3	1.0	-9.0	-1.3	1.0	%
$\Delta V_{OQ(\Delta T)}^{\dagger}$	-	_	±10	_	_	±10	-	_	±10	_	_	±10	G
Ratiometry, $\Delta V_{OQ(\Delta V)}$	-	100	_	_	100	_	-	100	_	_	100	_	%
Ratiometry, $\Delta Sens_{(\Delta V)}$	-	100	_	_	100	_	-	100	_	_	100	_	%
Positive Linearity, Lin+	_	100	_	_	100	_	_	100	_	_	100	_	%
Negative Linearity, Lin–	-	100	_	_	100	_	-	100	_	_	100	_	%
Symmetry	_	100	_	_	100	_	-	100	_	_	100	_	%

NOTE 1 – Magnetic flux density is measured at most sensitive area of device located 0.0195" (0.50 mm) below the branded face of the "UA" package.

NOTE 2 - 10 G = 1 mT, exactly.

NOTE 3 – Except for $\Delta \text{Sens}_{(\Delta T)}$, typical data is at $T_A = 25^{\circ}\text{C}$ and is for design information only.

* See Characteristics Definitions for test conditions.

† This calculation (formula 1, next page) yields the device's equivalent accuracy, over the operating temperature range, in gauss.



3515 AND 3516 *RATIOMETRIC, LINEAR HALL-EFFECT SENSORS*

CHARACTERISTICS DEFINITIONS

Quiescent Voltage Output. In the quiescent state (no magnetic field), the output is ideally equal to one-half of the supply voltage over the operating voltage and temperature range $(V_{OQ} \cup V_{CC}/2)$. Due to internal component tolerances and thermal considerations, there is a tolerance on the quiescent voltage output and on the quiescent voltage output as a function of supply voltage and ambient temperature. For purposes of specification, the quiescent voltage output as a function of temperature is defined as

$$\Delta V_{OQ(\Delta T)} = \frac{V_{OQ(TA)} - V_{OQ(25^{\circ}C)}}{Sens_{(25^{\circ}C)}}$$
(1)

This calculation yields the device's equivalent accuracy, over the operating temperature range, in gauss.

Sensitivity. The presence of a south-pole magnetic field perpendicular to the package face (the branded surface) will increase the output voltage from its quiescent value toward the supply voltage rail by an amount proportional to the magnetic field applied. Conversely, the application of a north pole will decrease the output voltage from its quiescent value. This proportionality is specified as the sensitivity of the device and is defined as

Sens =
$$\frac{V_{O(500G)} - V_{O(-500G)}}{1000 G}$$
 (2)

The stability of sensitivity as a function of temperature is defined as

$$\Delta \text{Sens}_{(\Delta \text{T})} = \frac{\text{Sens}_{(\text{TA})} - \text{Sens}_{(25^{\circ}\text{C})}}{\text{Sens}_{(25^{\circ}\text{C})}} \times 100\%$$
(3)

Ratiometry. The A3515xUA and A3516xUA feature a ratiometric output. The quiescent voltage output and sensitivity are proportional to the supply voltage (ratiometric).

The per cent ratiometric change in the quiescent voltage output is defined as

$$\Delta V_{OQ(\Delta V)} = \frac{V_{OQ(VCC)} / V_{OQ(5V)}}{V_{CC} / 5 V} \times 100\%$$
(4)

and the per cent ratiometric change in sensitivity is defined as

$$\Delta \text{Sens}_{(\Delta \text{V})} = \frac{\text{Sens}_{(\text{VCC})} / \text{Sens}_{(5\text{V})}}{\text{V}_{\text{CC}} / 5 \text{ V}} \times 100\%$$
(5)

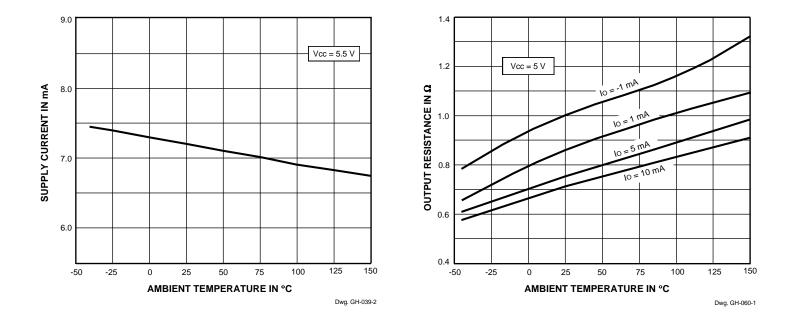
Linearity and Symmetry. The on-chip output stage is designed to provide a linear output to within 500 mV of either rail with a supply voltage of 5 V. This is equivalent to approximately ± 800 gauss of ambient field. Although application of stronger magnetic fields will not damage these devices, it will force the output into a non-linear region. Linearity in per cent is measured and defined as

$$\operatorname{Lin}_{+} = \frac{\operatorname{V}_{O(500\mathrm{G})} - \operatorname{V}_{OQ}}{2\left(\operatorname{V}_{O(250\mathrm{G})} - \operatorname{V}_{OQ}\right)} \times 100\%$$
(6)

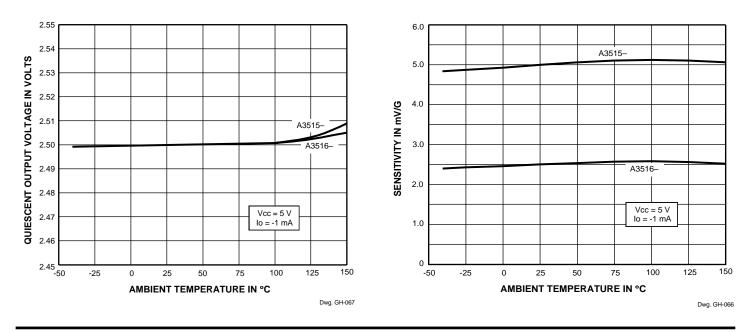
$$\text{Lin} = \frac{V_{\text{O}(-500\text{G})} - V_{\text{O}\text{Q}}}{2 \left(V_{\text{O}(-250\text{G})} - V_{\text{O}\text{Q}} \right)} \times 100\%$$
(7)

and output symmetry as

$$Sym = \frac{V_{O(500G)} - V_{OQ}}{V_{OQ} - V_{O(-500G)}} \times 100\%$$
(8)



TYPICAL CHARACTERISTICS





3515 AND 3516 RATIOMETRIC, LINEAR HALL-EFFECT SENSORS

APPLICATIONS INFORMATION

Calibrated linear Hall devices, which can be used to determine the actual flux density presented to the sensor in a particular application, are available.

For safe, reliable operation, the output should not be pulled above the supply voltage or pulled below the device ground.

For optimum performance, a 0.1 μ F capacitor between the supply and ground, and a 100 pF capacitor between the output and ground, should be added.

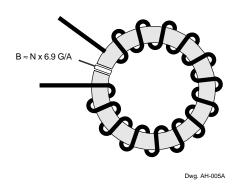
The ratiometric feature is especially valuable when these devices are used with an analog-to-digital converter. A/D converters typically derive their LSB step size by ratioing off a reference voltage line. If the reference voltage varies, the LSB will vary proportionally. This is a major error source in many sensing systems. The A3515xUA and A3516xUA can eliminate this source of error by their ratiometric operation. Because their gain and offsets are proportional to the supply voltage, if they are powered from the A/D reference voltage, the sensor output voltage will track changes in the LSB value.

These devices can withstand infrequent temperature excursions, beyond the Absolute Maximum Ratings, to $T_A = 170^{\circ}C$ provided the junction temperature, T_J , does not exceed 200°C.

Extensive applications information on Hall-effect sensors and magnets is also available in the "Hall-Effect IC Applications Guide", which can be found in the latest issue of the *Allegro MicroSystems Electronic Data Book*, AMS-702 or *Application Note* 27701, or at

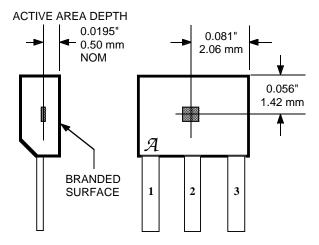
www.allegromicro.com

TYPICAL CURRENT-SENSING APPLICATION

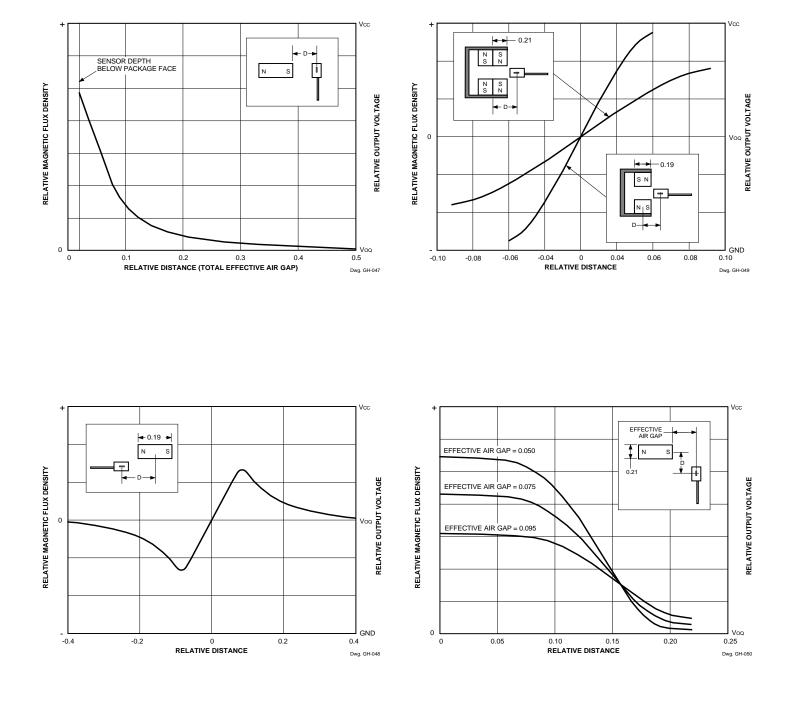


SENSOR LOCATION

SUFFIX "UA"



Dwg. MH-011-7A

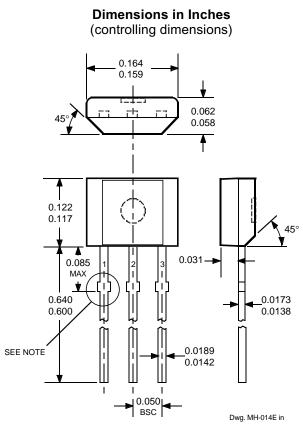


TYPICAL POSITION-SENSING APPLICATIONS

(Alnico 8, dimensions in inches)

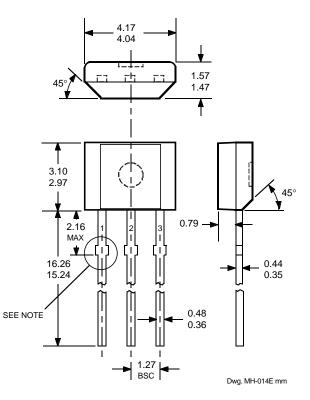


3515 AND 3516 RATIOMETRIC, LINEAR HALL-EFFECT SENSORS

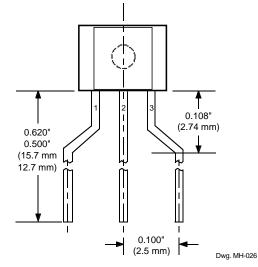


A3515xUA and A3516xUA

Dimensions in Millimeters (for reference only)



Radial Lead Form (order A351xxUA-LC)



NOTE: Lead-form dimensions are the nominals produced on the forming equipment. No dimensional tolerance is implied or guaranteed for bulk packaging (500 pieces per bag).

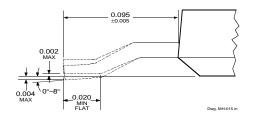
NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).

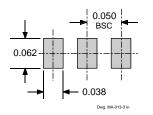
- 2. Exact body and lead configuration at vendor's option within limits shown.
- 3. Height does not include mold gate flash.
- 4. Recommended minimum PWB hole diameter to clear transition area is 0.035" (0.89 mm).
- 5. Where no tolerance is specified, dimension is nominal.
- 6. Supplied in bulk pack (500 pieces per bag).

Surface-Mount Lead Form (order A351xxUA-TL)

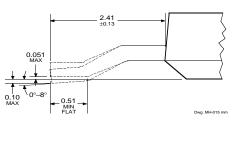
Dimensions in Inches (controlling dimensions)

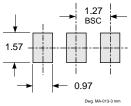
Dimensions in Millimeters (for reference only)





NOTE: Supplied in bulk pack (500 devices per bag).





The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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